

Scalable Matrix Based **Error Mitigation**

0.06 0.03 0.04

0.03

0.92

0.05 0.02 0.89/

0.85

0.04

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0.02

0.05



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Introduction and Motivation

In the noisy intermediate-scale quantum (NISQ) era, quantum computers are often too noisy for meaningful computation.

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 \swarrow

 $|0\rangle - X - \langle 0|$

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0.85

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for SPAM protocol.

- Error correction requires too many qubits with high fidelity.
- We can use classical processors to enhance the quality of the results after computation on a quantum processor \Rightarrow Error mitigation.



Scalable Mitigation Method

How can we make the error mitigation scalable to large qubit numbers?

- Observation: Complete assignment matrix has a lot of entries close to 0
- (see dark parts in Fig 3).
- Idea: Define a threshold \bullet and only consider larger entries for mitigation. Problem: We cannot create the full matrix.



that mitigates SPAM errors. It is used, for example, in Qiskit [1].

- Set up all possible basis states.
- Perform measurement.
- Create complete assignment matrix M $|0\rangle - X - \langle 0|$ (see Fig 1).



- No gate errors considered!
- Motivation for this work is to include gate errors and make the mitigation scalable.

$$\min f(x) = \sum_{i=1}^{\infty} (v_i - (MX)_i)^2 \quad (1)$$

General error mitigation [2] improves SPAM protocol to consider gate errors as well.

- Split circuits in two parts.
- Invert both circuits.
- Create complete assignment matrix. Use least squares method to mitigate error. Exponential number of calibration <u>circuits (2^{N+1}) required!</u>



- Idea: Run circuit and only \bullet consider *k* largest states in the output for mitigation.
- How to choose the optimal *k*?
- We can calculate a score \bullet that quantifies the quality of mitigation: ΔQ (see below for detail).
- There is an optimum be- \bullet tween ΔQ and the matrix size if we use the true number of non-zero output states for mitigation (see Fig. 4).
- Idea: We increase *k* itera- \bullet tively until ΔQ becomes constant.

Fig 3: Average of 103 assignment matrices with 7 qubits plotted as a heat map. The columns represent the values of the measured frequencies.



Fig 4: ΔQ and the matrix size as a function of k. We see that the mitigation quality does not improve beyond k = 4, which is also the number of non-zero output states for this example.





Fig 2: Creation of complete assignment matrix for the general error mitigation protocol.

Experimental Results

We performed all tests on IBM Q superconducting devices.

- A total of 1853 random circuits were generated.
- 2-7 qubits with simulated data for comparison.
- Scaling test with 100 qubits (results not shown).

Results:

- Mitigation works comparably well
- The size of the sparse matrix is



- Problem: Calculating ΔQ requires the simulated frequencies.
- Solution: We introduce the score ΔR which replaces ΔQ . We no longer require simulated frequencies.



s: simulated frequencies *x*: mitigated frequencies *v*: measured frequencies

Fig 5: ΔR and ΔQ plotted as a function of *k*. We see that ΔR can serve as replacement for ΔQ , since the slopes of both go to zero at the same k.





 $\Delta Q = \Delta V - \Delta X$ (5)

Conclusion

- The new mitigation protocol \bullet builds upon a general matrix based error mitigation method. Our tests show that the \bullet
 - method performs comparably well while reducing the matrix size significantly.

References

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